Application No.	Applicant(s)	
10/688,385 Examiner	SWAMINATHAN, SIVAPRASATH Art Unit	
Paul B. Yanchus	2116	
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on 10/17/03.		
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	Examiner  Paul B. Yanchus  Pars on the cover sheet with the co (OR REMAINS) CLOSED in this apport of the appropriate communication GHTS. This application is subject to and MPEP 1308.  On 10/17/03.  Inder 35 U.S.C. § 119(a)-(d) or (f).  Inder been received. Inder been received in Application No cuments have been received in this in of this communication to file a reply lient of this application.  Inder 35 U.S.C. § 119(a)-(d) or (f).  Inder 35	10/688,385   SWAMINATHAN, SExaminer   Art Unit     Paul B. Yanchus   2116     Paul B. Yanchus   2116

Art Unit: 2116

## **DETAILED ACTION**

## Allowable Subject Matter

Claims 1-16 are allowed.

## Reasons For Allowance

The following is an examiner's statement of reasons for allowance:

Graf, US Patent no. 6,317,798 discloses initiation of a BIOS console redirection in a computer system.

Murphy, US Patent Application Publication no. 2004/0177245 discloses predicting whether a first resource configuration will be more efficient than a second resource configuration.

Magro et al., US Patent no. 6,546,482, discloses detecting conflicts between system resources of a microcontroller.

Mahmoud, US Patent no. 6,865,669, discloses allocating memory for BIOS components in a computer system.

The prior art of record does not teach or suggest determining that an operating system has allocated resources in a manner that indicates a conflict between a BIOS redirection component and the operating system may occur and preemptively releasing resources allocated to the BIOS redirection component to prevent the possible conflict.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

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fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Paul B. Yanchus whose telephone number is (571) 272-3678.

The examiner can normally be reached on Mon-Thurs 8:00-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Lynne H. Browne can be reached on (571) 272-3670. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paul Yanchus May 12, 2006

> JOHN R. COTTINGHAM PRIMARY EXAMINER